

Features

- 2V to 24V Input Voltage
- Up to 28V Output Voltage
- Integrated 80mΩ Power MOSFET
- 1.2MHz Fixed Switching Frequency
- Internal 4A Switch Current Limit
- Internal Compensation
- Thermal Shutdown
- Output Adjustable from 0.6V
- Available in SOT23-6 Package

Applications

- ABS Set-Top Boxed
- DVB-S/S2

General Description

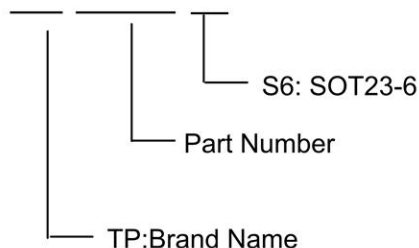
The TP7820S6 is a constant frequency, current mode step-up converter intended for small, low power applications. The TP7820S6 switches at 1.2MHz and allows the use of tiny, low cost capacitors and inductors 2mm or less in height. Internal soft-start results in small inrush current and extends battery life.

The TP7820S6 includes under-voltage lockout, current limiting, and thermal overload protection to prevent damage in the event of an output overload.

TP7820S6 is available in SOT23-6 package that is PB free.

Ordering Information

TP7820S6



TYPICAL APPLICATION

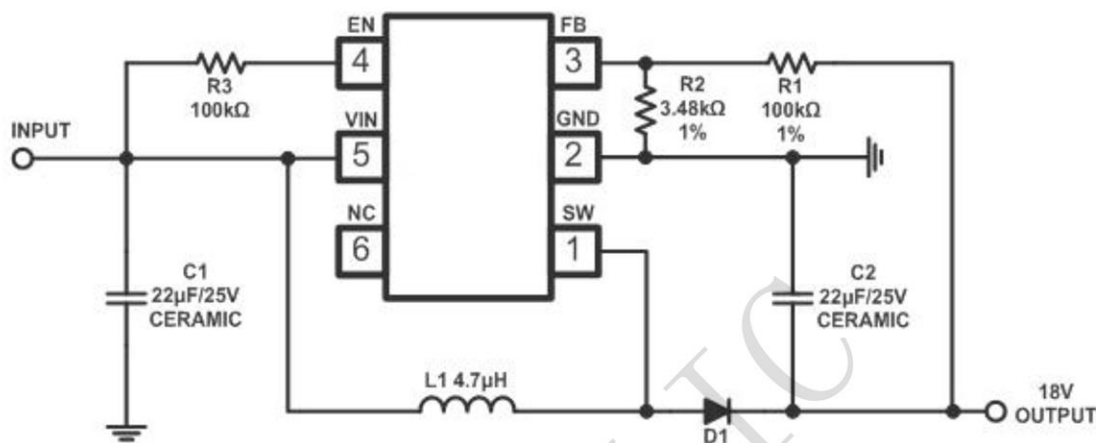
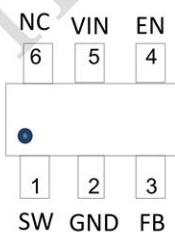


Figure1. Basic Application Circuit

PIN CONFIGURATION



Pin No.	Symbol	Description
1	SW	Power Switch Output. SW is the drain of the internal MOSFET switch. Connect the power inductor and output rectifier to SW. SW can swing between GND and 28V.
2	GND	Ground.
3	FB	Feedback Input. The FB voltage is 0.6V. Connect a resistor divider to FB.
4	EN	Regulator On/Off Control Input. A high input at EN turns on the converter, and a low input turns it off. When not used, connect EN to the input supply for automatic startup.
5	VIN	Power Supply. Must be locally bypassed.
6	NC	No Connection

Absolute Maximum Rating ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter		Value
VIN, EN Pin Voltage		-0.3V to 26V
SW Pin Voltage		-0.3V to 26V
All Other Pin Voltage		-0.3V to 6V
Junction Temperature (T _J)		150°C
Ambient Temperature (T _A)		-40°C to 85°C
Power Dissipation	SOT23-6	600mW
Thermal Resistance (θ _{JA})		250°C/W
Thermal Resistance (θ _{JC})		130°C/W
Storage Temperature (Ts)		-65°C to 150°C
Lead Temperature & Time		260°C, 10Sec

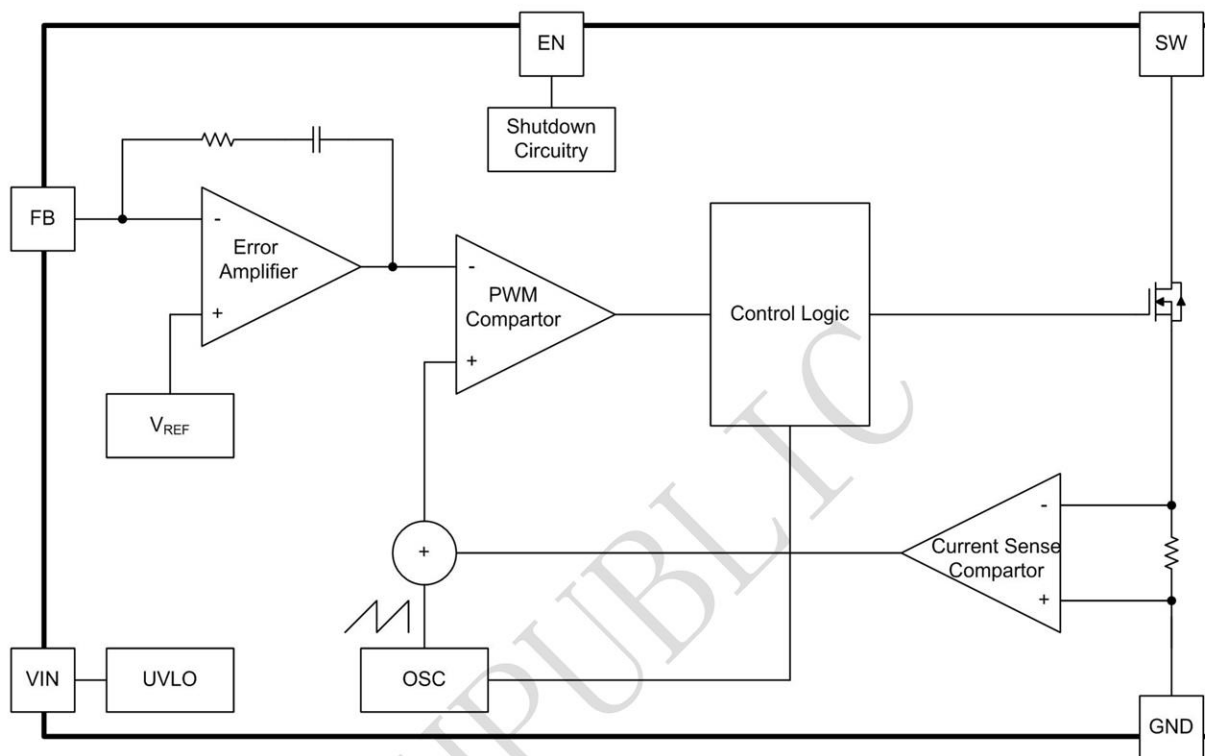
Recommended Operating Conditions

Parameter	Value
Input Voltage Range	2V to 24V
Output Voltage Range	VIN to 28V
Operating Junction Temperature(T_J)	-40°C –125°C

Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

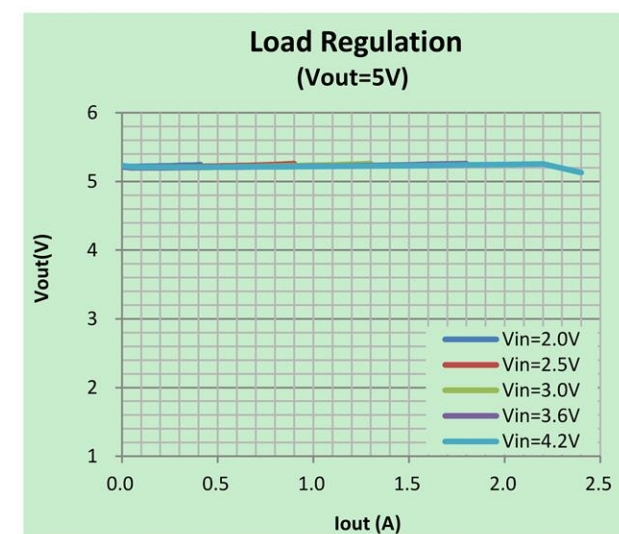
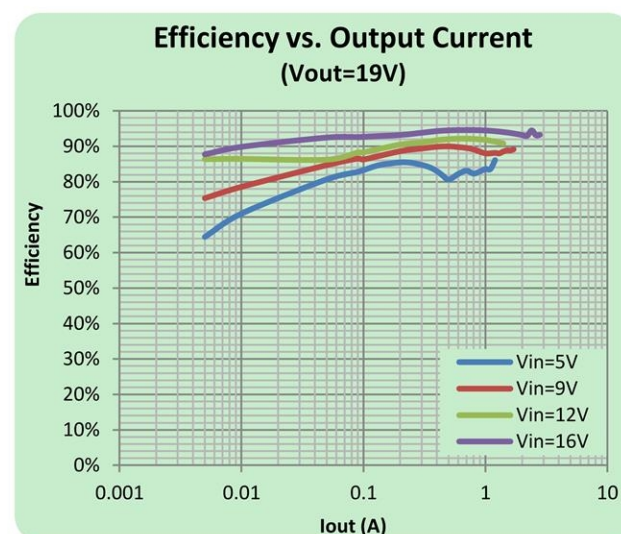
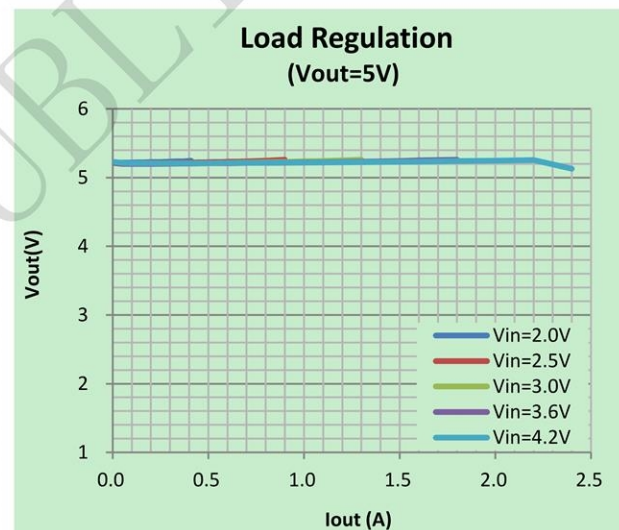
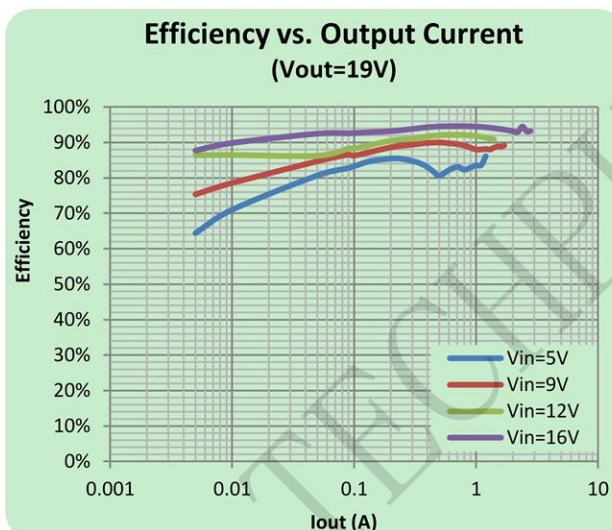
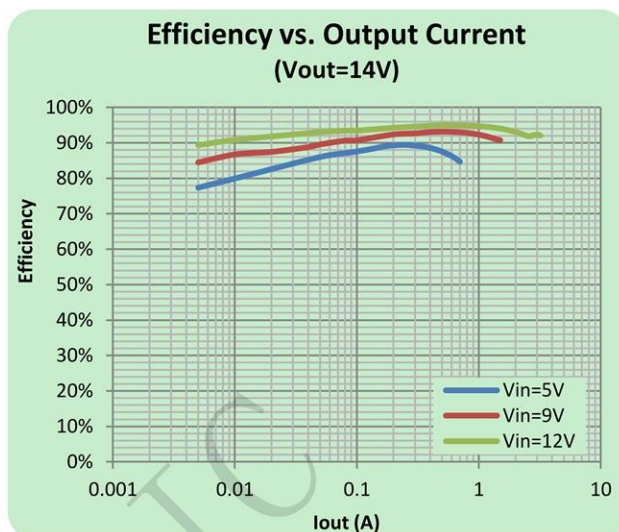
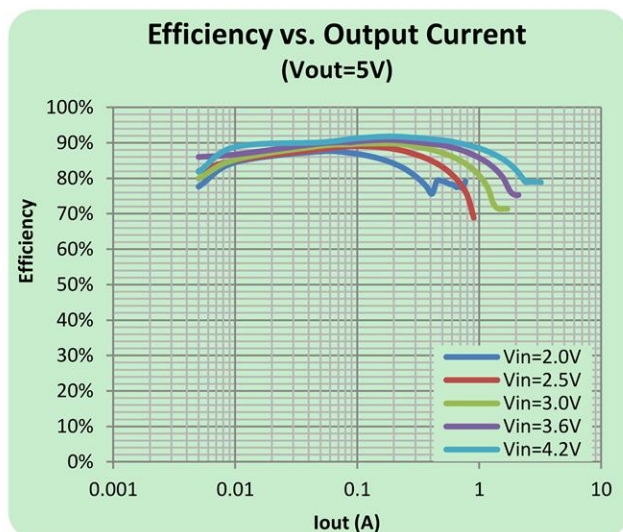
Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V_{IN}	Operating Input Voltage		2		24	V
V_{FB}	Feedback Voltage		588	600	612	mV
I_{FB}	FB input Bias Current	$V_{FB}=0.6\text{V}$	-50	-10		nA
	SW Leakage	$V_{SW}=20\text{V}$			1	uA
I_Q	Quiescent Current	$V_{FB}=0.5\text{V}$, Switch		0.2	0.4	mA
		$V_{EN}=0\text{V}$		0.1	1	uA
F_{SW}	Oscillator Frequency	$V_{FB}=0.75\text{V}$		1.2		MHz
D_{MAX}	Maximum Duty Cycle	$V_{FB}=0.7\text{V}$		90		%
V_{EN}	EN Threshold			1		V
	SW On-Resistance			80	150	mΩ
I_{LIMIT}	Current Limit	$V_{IN}=5\text{V}$, Duty Cycle = 50%		4		A
	Thermal Shutdown			160		°C

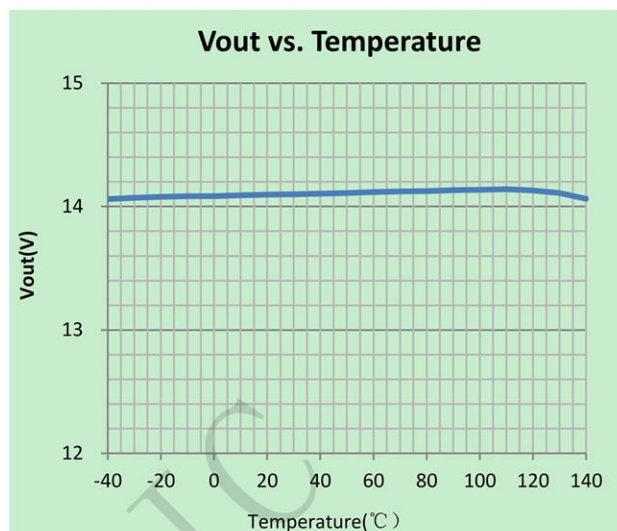
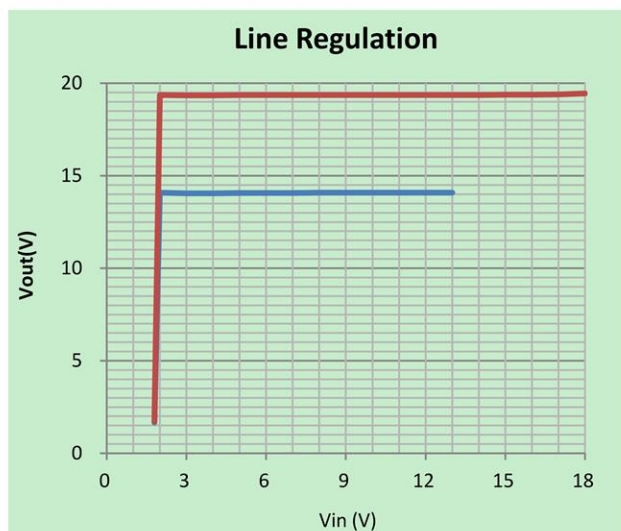
BLOCK DIAGRAM



TYPICAL PERFORMANCE CHARACTERISTICS

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DETAILED DESCRIPTION

The TP7820 uses a fixed frequency, peak current mode boost regulator architecture to regulate voltage at the feedback pin. The operation of the TP7820 can be understood by referring to the block diagram of Figure 3. At the start of each oscillator cycle the MOSFET is turned on through the control circuitry. To prevent sub-harmonic oscillations at duty cycles greater than 50 percent, a stabilizing ramp is added to the output of the current sense amplifier and the result is fed into the negative input of the PWM comparator. When this voltage equals the output voltage of the error amplifier the power MOSFET is turned off. The voltage at the output of the error amplifier is an amplified version of the difference between the 0.6V band gap reference voltage and the feedback voltage. In this way the peak current level keeps the output in regulation. If the feedback voltage starts to drop, the output of the error amplifier increases. These results in more current to flow through the power MOSFET, thus increasing the power delivered to the output. The TP7820 has internal soft start to limit the amount of input current at startup and to also limit the amount of overshoot on the output.

APPLICATION INFORMATION

Setting the Output Voltage

The internal reference V_{REF} is 0.6V (Typical). The output voltage is divided by a resistor divider, R_1 and R_2 to the FB pin. The output voltage is given by

$$V_{OUT} = V_{REF} \times \left(1 + \frac{R_1}{R_2}\right)$$

Inductor Selection

The recommended values of inductor are 4.7 to 22 μ H. Small size and better efficiency are the major concerns for portable device, such as TP7820 used for mobile phone. The inductor should have low core loss at 1.2MHz and low DCR for better efficiency. To avoid inductor saturation current rating should be considered.

Capacitor Selection

Input and output ceramic capacitors of 22 μ F are recommended for TP7820 applications. For better voltage filtering, ceramic capacitors with low ESR are recommended. X5R and X7R types are suitable because of their wider voltage and temperature ranges.

Diode Selection

Schottky diode is a good choice for TP7820 because of its low forward voltage drop and fast reverses recovery. Using Schottky diode can get better efficiency. The high speed rectification is

also a good characteristic of Schottky diode for high switching frequency. Current rating of the diode must meet the root mean square of the peak current and output average current multiplication as following:

$$I_D(RMS) \approx \sqrt{I_{OUT} \times I_{PEAK}}$$

The diode's reverse breakdown voltage should be larger than the output voltage.

Layout Consideration

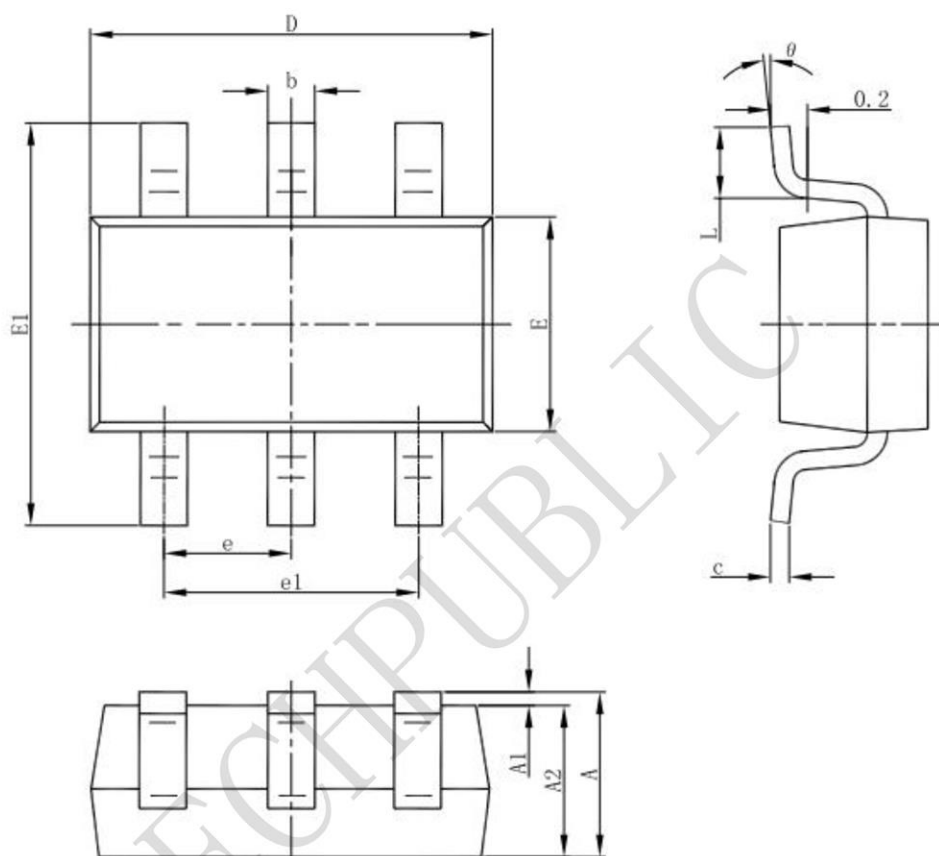
When laying out the printed circuit board, the following checking should be used to ensure proper operation of the TP7820.

Check the following in your layout:

- 1) The power traces, consisting of the GND trace, the SW trace and the VIN, trace should be kept short, direct and wide.
- 2) Does the (+) plates of C_{in} connect to V_{in} as closely as possible. This capacitor provides the AC current to the internal power MOSFETs.
- 3) Keep the switching node SW away from the sensitive VOUT node.
- 4) Keep the (-) plates of C_{in} and C_{out} as close as possible

Package information

SOT23-6



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°